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Patent

Customer No. 31561 Docket No. 10958-US-PA Application No.: 10/604,651

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Applicant

: Peng et al.

Application No. Filed

: 10/604,651 : August 7, 2003

For

: LOW TEMPERATURE POLYSILICON THIN FILM

TRANSISTOR AND METHOD OF MANUFACTURING

THE SAME

Examiner

: GHYKA, ALEXANDER G.

Art Unit

: 2812 INFORMATION DISCLOSURE STATEMENT

+1-571-273-8300 (Via fax: 2+14 pages)

United States Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building

401 Dulany Street Alexandria, VA 22314

Dear Sirs,

Enclosed is a PTO Form 1449 listing 1 reference(s), copy of which is attached. Applicant submits the reference(s) in compliance with his duty of disclosure pursuant to 37 CFR § 1.56 and 1.97. The Examiner is requested to make the citation(s) of official record.

The IDS is being submitted after the mailing date of a final Office Action. In addition, the reference(s) was first cited in a counterpart foreign application more than three months prior to the filing of the IDS. Accordingly, please place the IDS in the file instead of having it considered.

The submission of the reference(s) should not be interpreted as admitting them as prior art.

Respectfully Submitted,

JIANO CHYUN Intellectual Property Office

Date: Dec. 21 1705

By: Delinda Lee

Registration No.: 46,863

Please send future correspondence to:

7F. -1, No. 100, Roosevelt Rd., Sec. 2, Taipei 100, Taiwan, R.O.C. Tel: 886-2-2369 2800 Fax: 886-2-2369 7233 / 886-2-2369 7234

E-MAIL: BELINDA@JCIPGroup.com.tw; USA@JCIPGroup.com.tw

FORM PTO-1449  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICATION			ATTY. DOCKET NO.:10958-US-PA APPLICATION NO.: 10/604,651  APPLICANT: Peng et al.				
					U.S. PATENT	r Doucments	
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCL	ASS	FIILNG DAT (IF APPROPRIA
		FOREIGN PAT	ENT DOCUMENTS			1 2	
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	(IF APPROPRIATE)	
	2001-345447	December 14, 200	Japan Japan		May 31, 2000		ay 31, 2000
				-			
						<u> </u>	
EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)						
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